FIG. 1

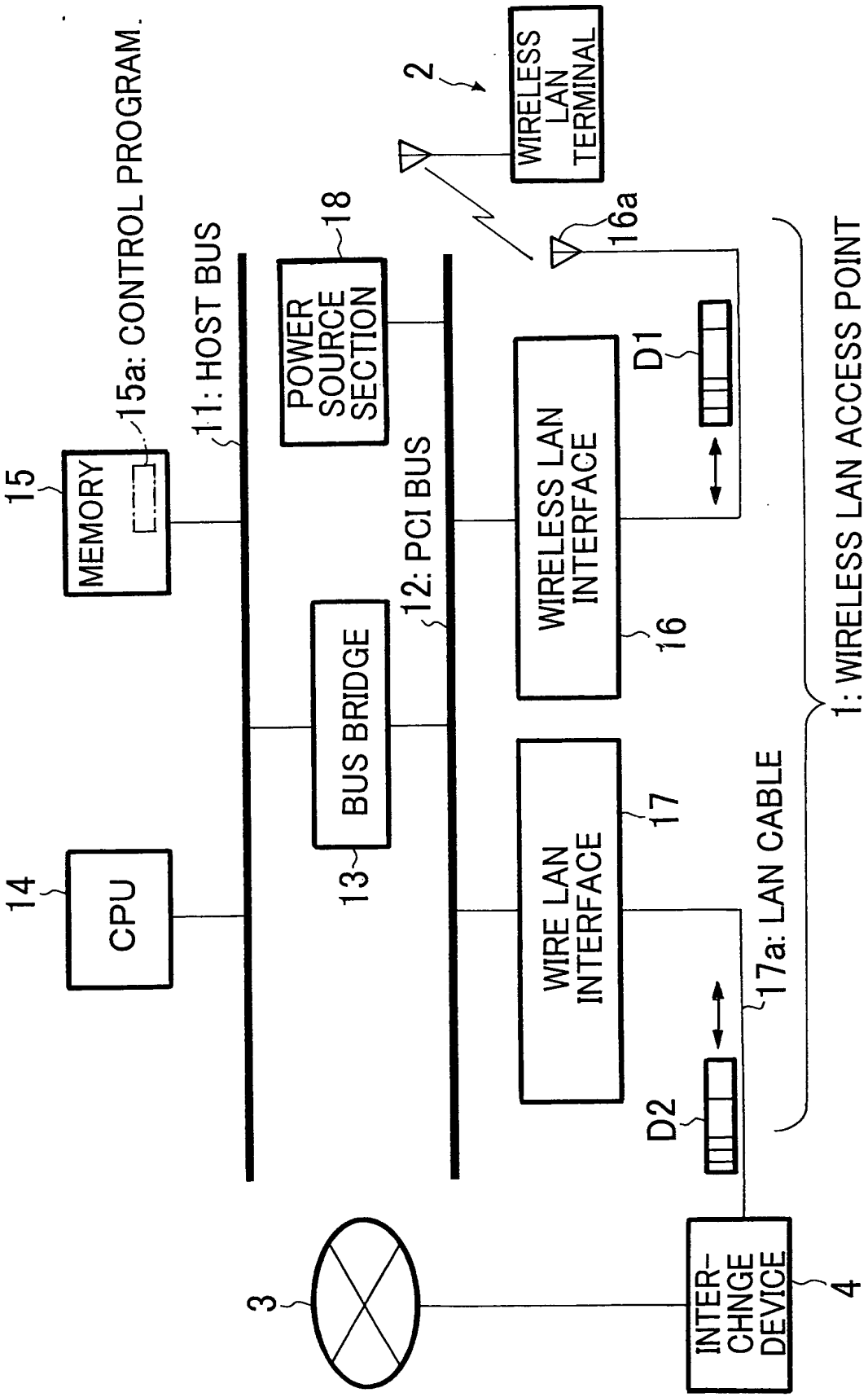

1: WIRELESS LAN ACCESS POINT
2
WIRELESS LAN TERMINAL
16a
D1
WIRELESS LAN INTERFACE
16
17
WIRE LAN INTERFACE
17a: LAN CABLE
D2
18
POWER SOURCE SECTION
11: HOST BUS
12: PCI BUS
13
BUS BRIDGE
15
MEMORY
15a: CONTROL PROGRAM.
14
CPU
3
4
INTER-CHNGE DEVICE

FIG.2

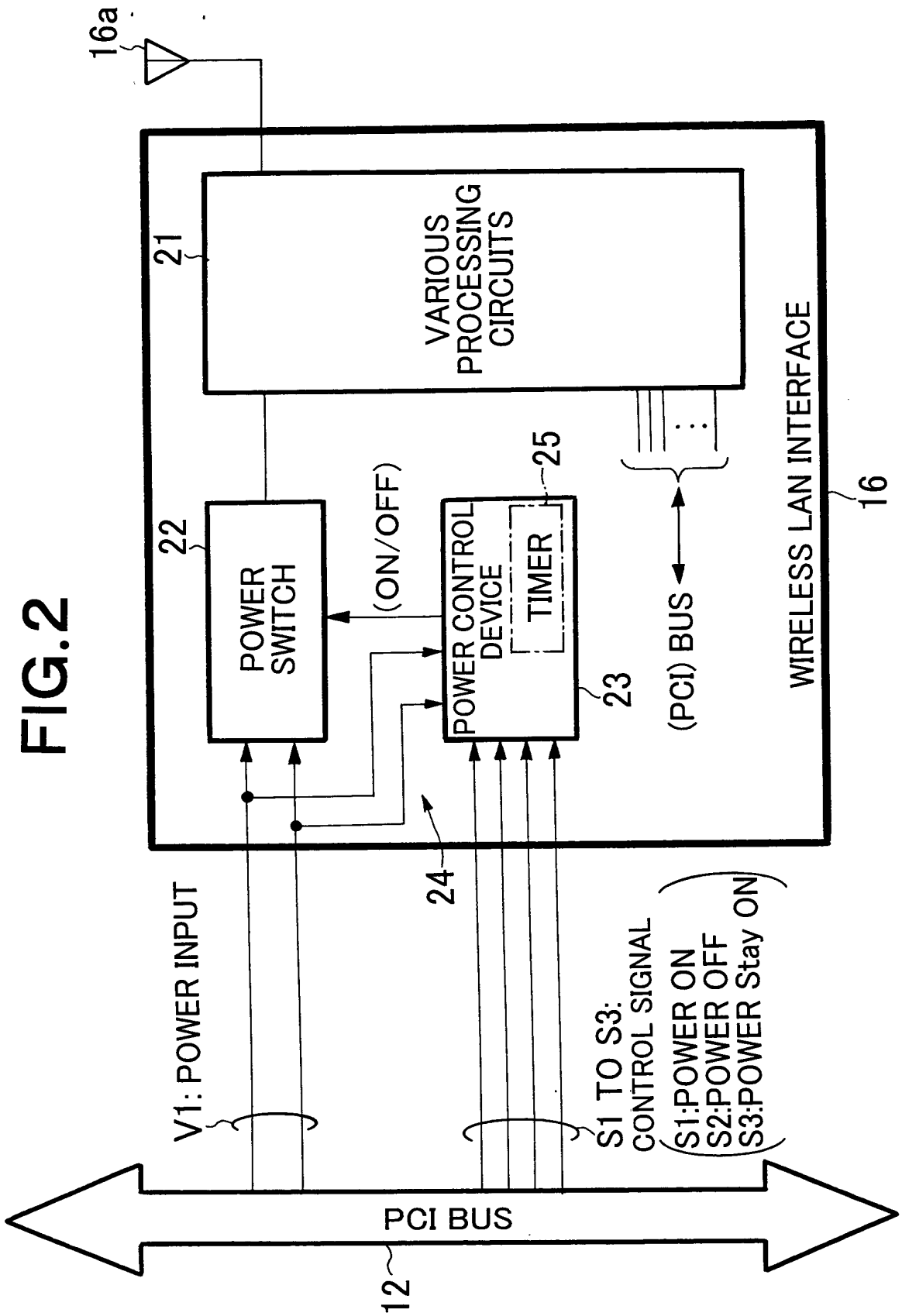

16a
21
VARIOUS PROCESSING CIRCUITS
WIRELESS LAN INTERFACE
16
22
POWER SWITCH
(ON/OFF)
25
POWER CONTROL DEVICE
TIMER
23
(PCI) BUS
24
V1: POWER INPUT
S1 TO S3: CONTROL SIGNAL
S1:POWER ON
S2:POWER OFF
S3:POWER Stay ON
PCI BUS
12

FIG.3

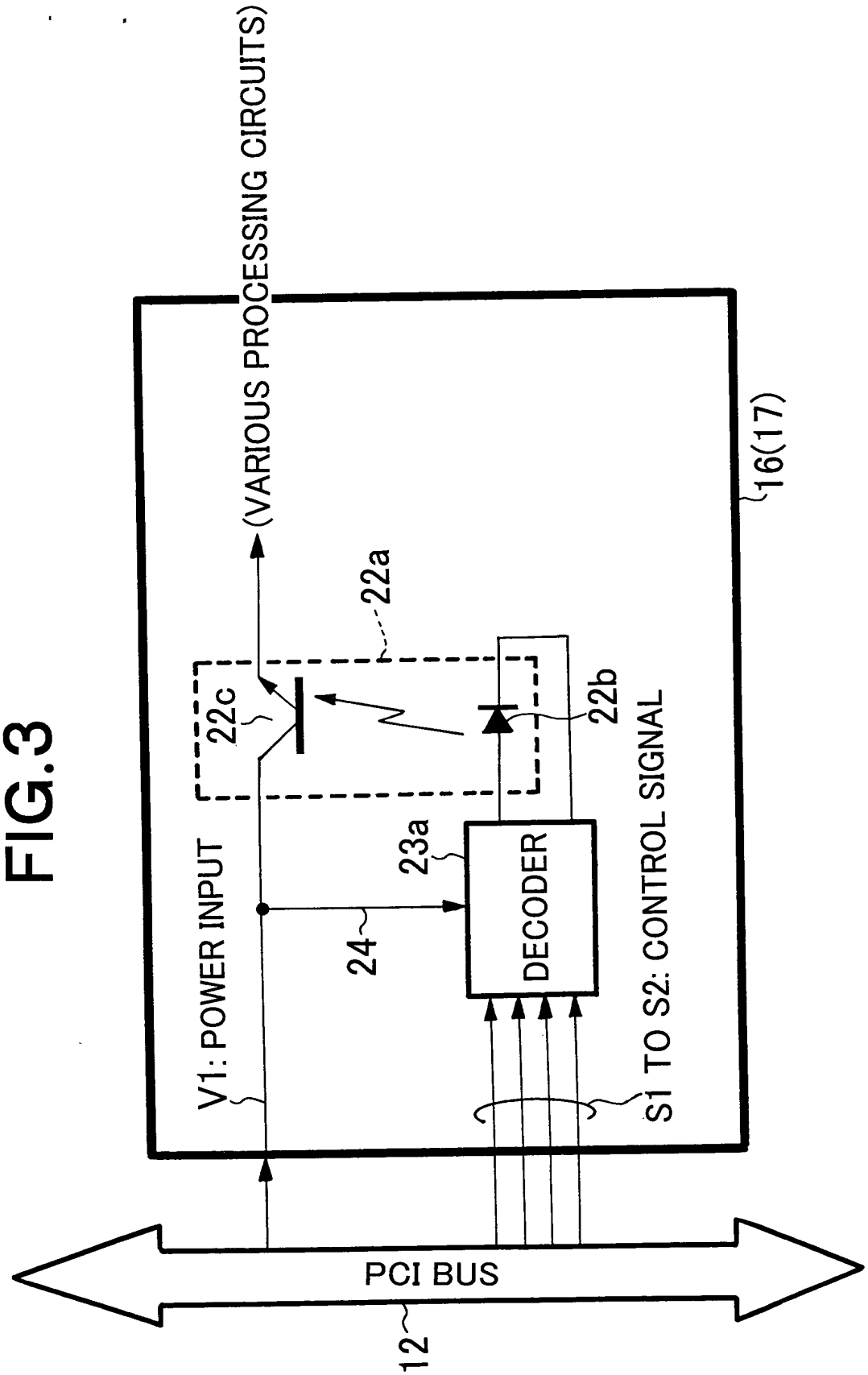

(VARIOUS PROCESSING CIRCUITS)
16(17)
22a
22c
22b
23a
DECODER
24
V1: POWER INPUT
S1 TO S2: CONTROL SIGNAL
PCI BUS
12

FIG.4

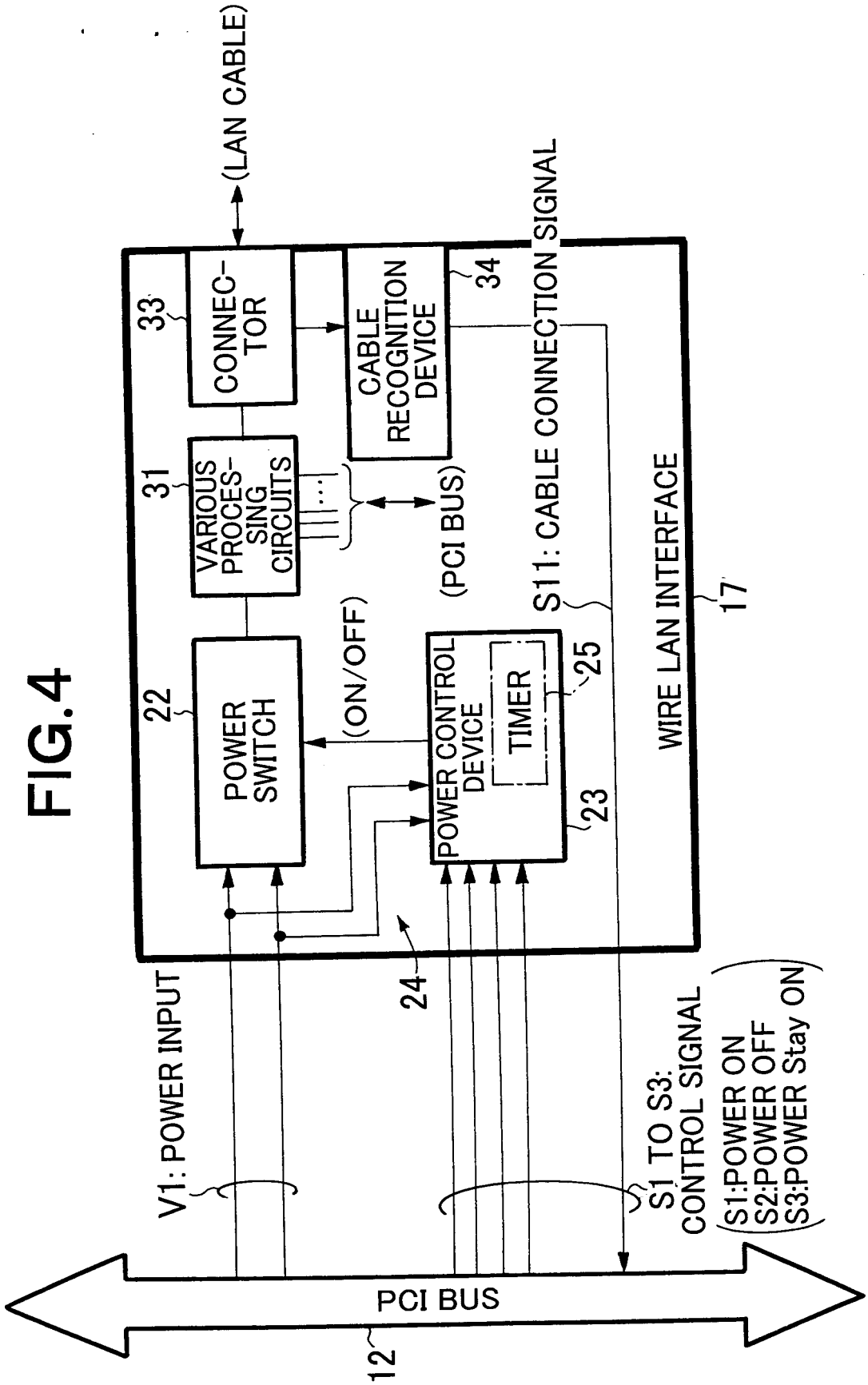

(LAN CABLE)
33
CONNEC-TOR
CABLE RECOGNITION DEVICE
34
S11: CABLE CONNECTION SIGNAL
31
VARIOUS PROCES-SING CIRCUITS
(PCI BUS)
WIRE LAN INTERFACE
17
22
POWER SWITCH
(ON/OFF)
POWER CONTROL DEVICE
TIMER
25
23
24
V1: POWER INPUT
S1 TO S3: CONTROL SIGNAL
(S1:POWER ON
S2:POWER OFF
S3:POWER Stay ON)
PCI BUS
12

FIG.5

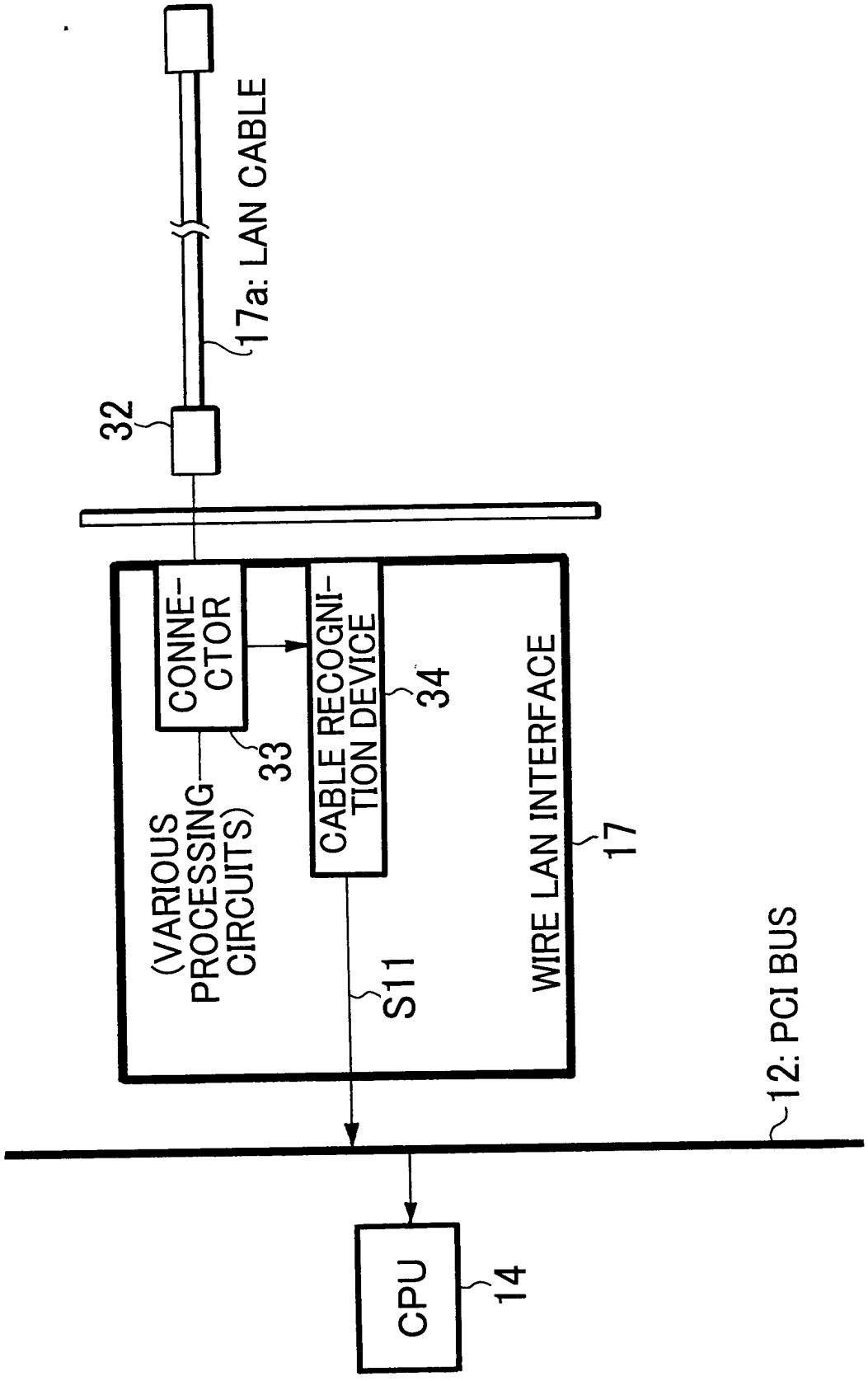

17a: LAN CABLE
32
CONNE-CTOR
33
CABLE RECOGNI-TION DEVICE
34
(VARIOUS PROCESSING CIRCUITS)
S11
WIRE LAN INTERFACE
17
12: PCI BUS
CPU
14

FIG.6

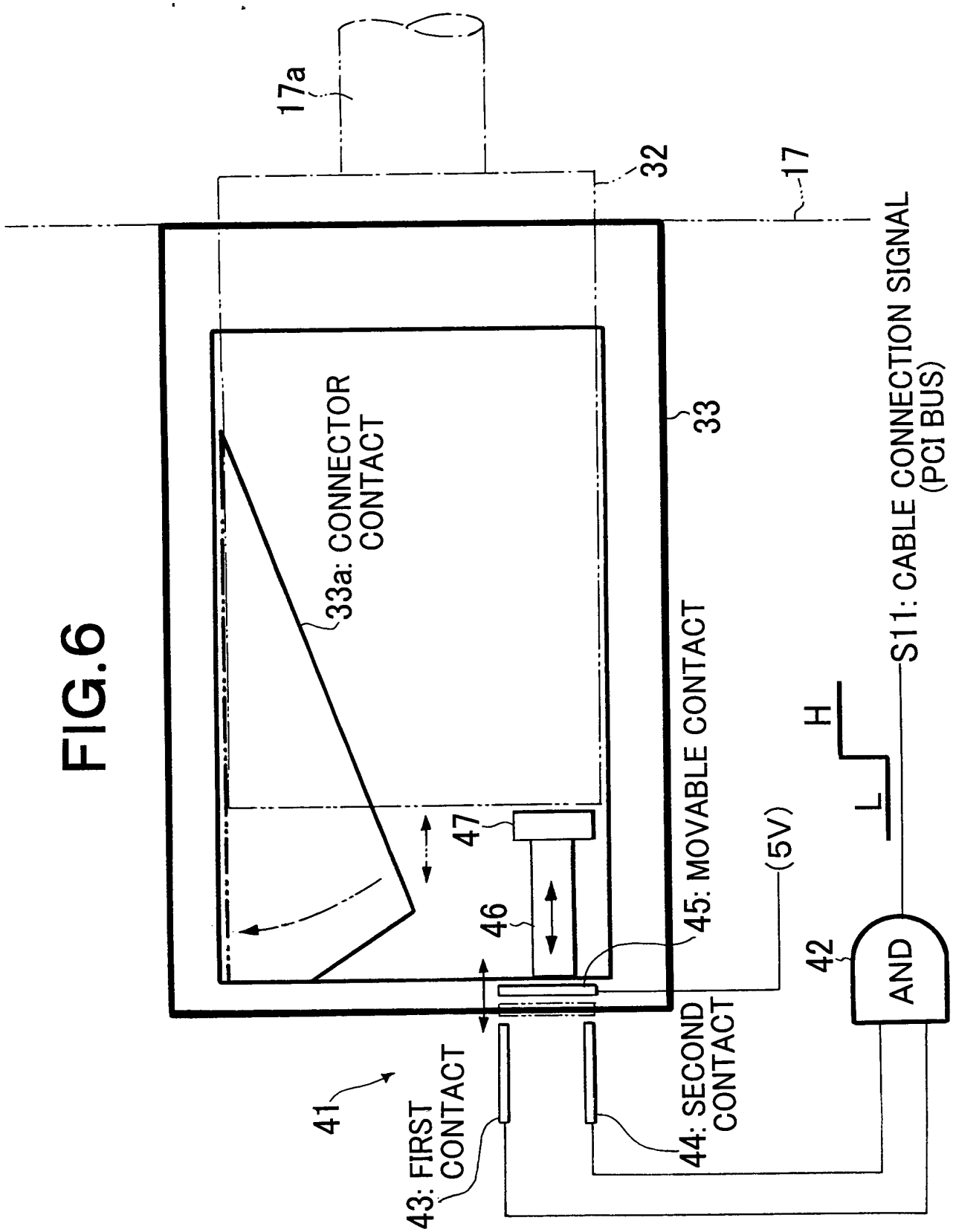

17a
32
17
33
33a: CONNECTOR CONTACT
47
46
45: MOVABLE CONTACT
(5V)
H
L
S11: CABLE CONNECTION SIGNAL (PCI BUS)
42
AND
41
43: FIRST CONTACT
44: SECOND CONTACT

FIG.7

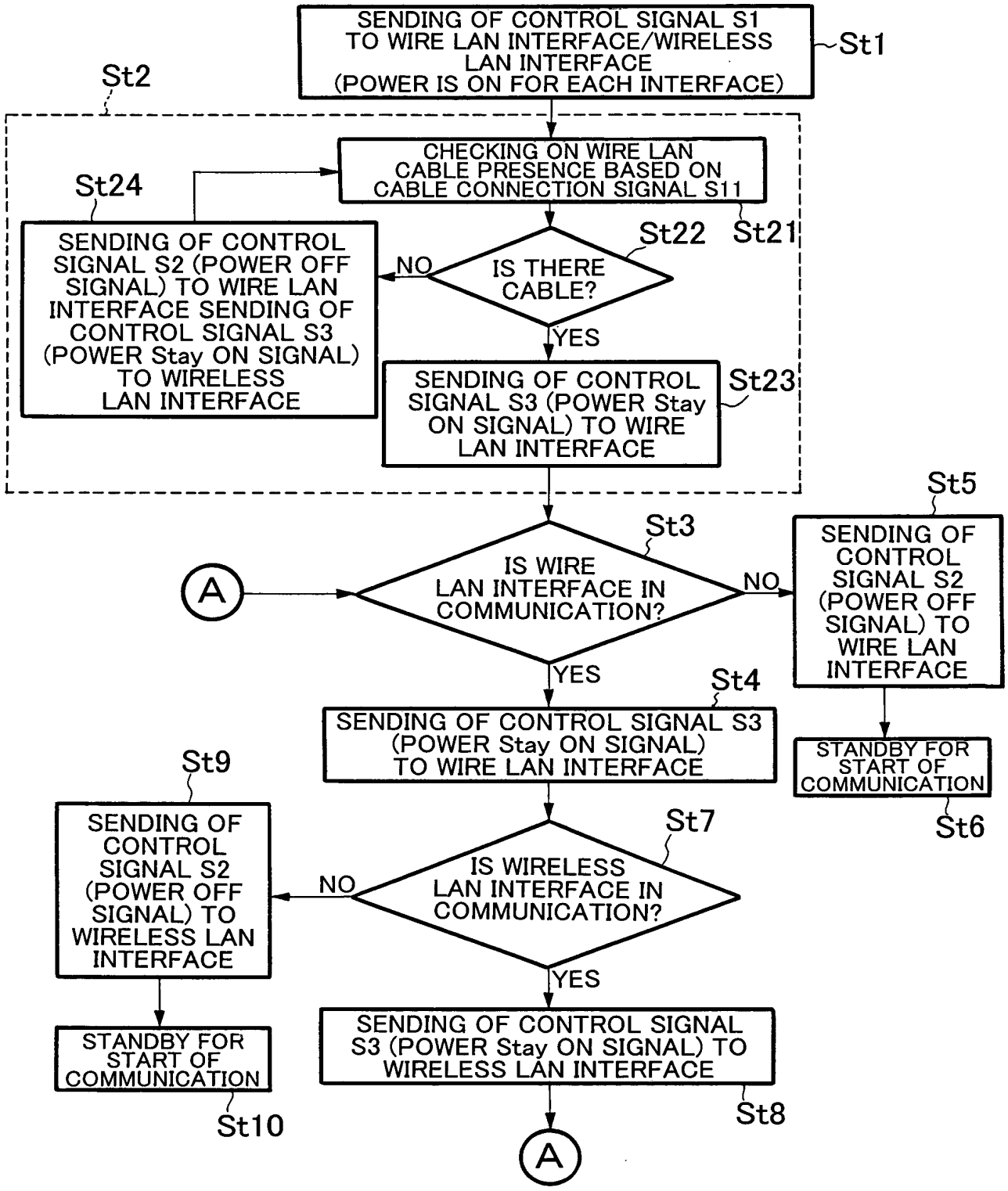

SENDING OF CONTROL SIGNAL S1 TO WIRE LAN INTERFACE/WIRELESS LAN INTERFACE (POWER IS ON FOR EACH INTERFACE)
St1
St2
CHECKING ON WIRE LAN CABLE PRESENCE BASED ON CABLE CONNECTION SIGNAL S11
St21
St22
IS THERE CABLE?
NO
YES
St24
SENDING OF CONTROL SIGNAL S2 (POWER OFF SIGNAL) TO WIRE LAN INTERFACE SENDING OF CONTROL SIGNAL S3 (POWER Stay ON SIGNAL) TO WIRELESS LAN INTERFACE
SENDING OF CONTROL SIGNAL S3 (POWER Stay ON SIGNAL) TO WIRE LAN INTERFACE
St23
St5
St3
A
IS WIRE LAN INTERFACE IN COMMUNICATION?
NO
YES
SENDING OF CONTROL SIGNAL S2 (POWER OFF SIGNAL) TO WIRE LAN INTERFACE
St4
SENDING OF CONTROL SIGNAL S3 (POWER Stay ON SIGNAL) TO WIRE LAN INTERFACE
STANDBY FOR START OF COMMUNICATION
St6
St9
St7
SENDING OF CONTROL SIGNAL S2 (POWER OFF SIGNAL) TO WIRELESS LAN INTERFACE
NO
IS WIRELESS LAN INTERFACE IN COMMUNICATION?
YES
STANDBY FOR START OF COMMUNICATION
St10
SENDING OF CONTROL SIGNAL S3 (POWER Stay ON SIGNAL) TO WIRELESS LAN INTERFACE
St8
A

FIG.8

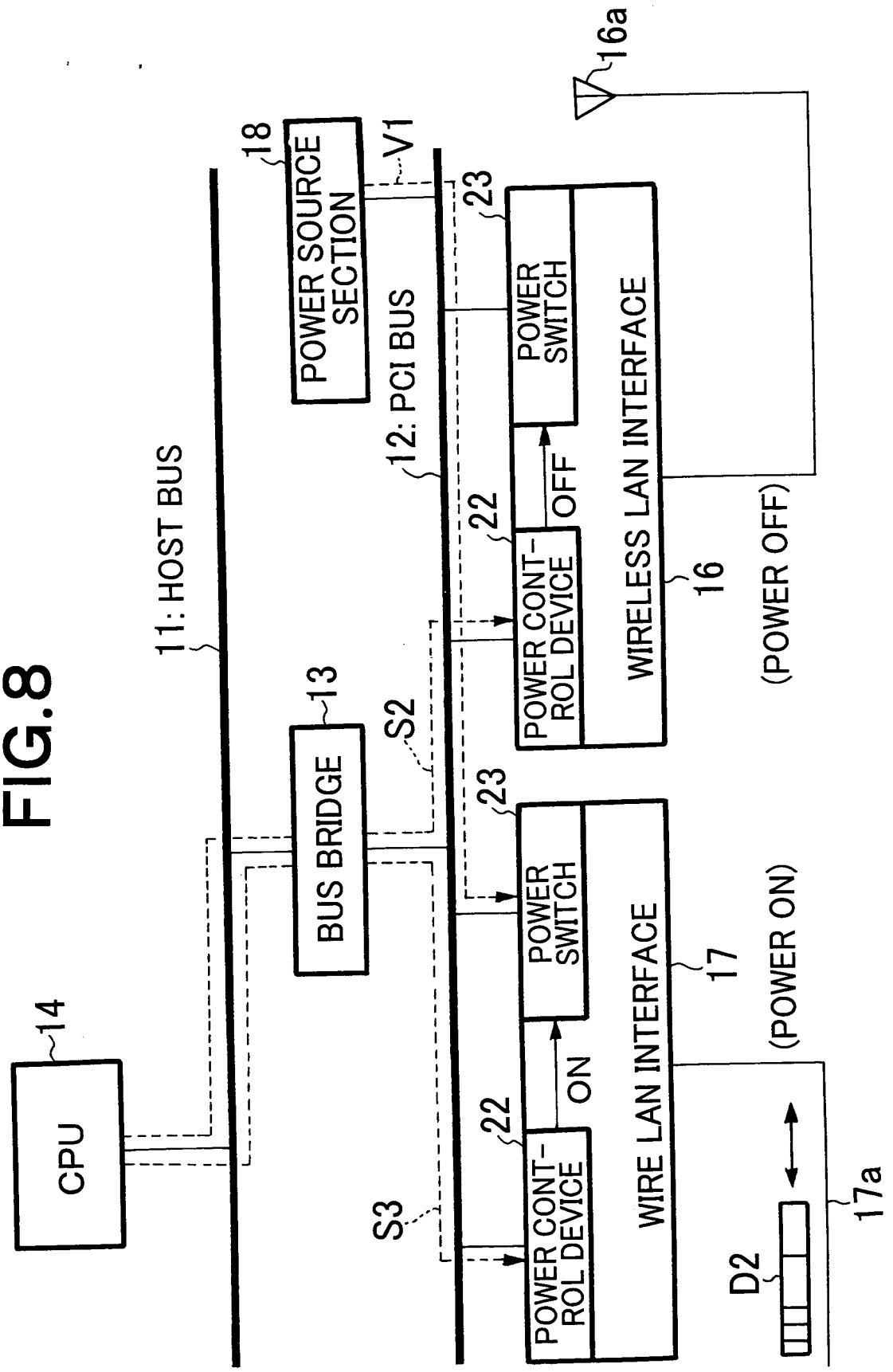
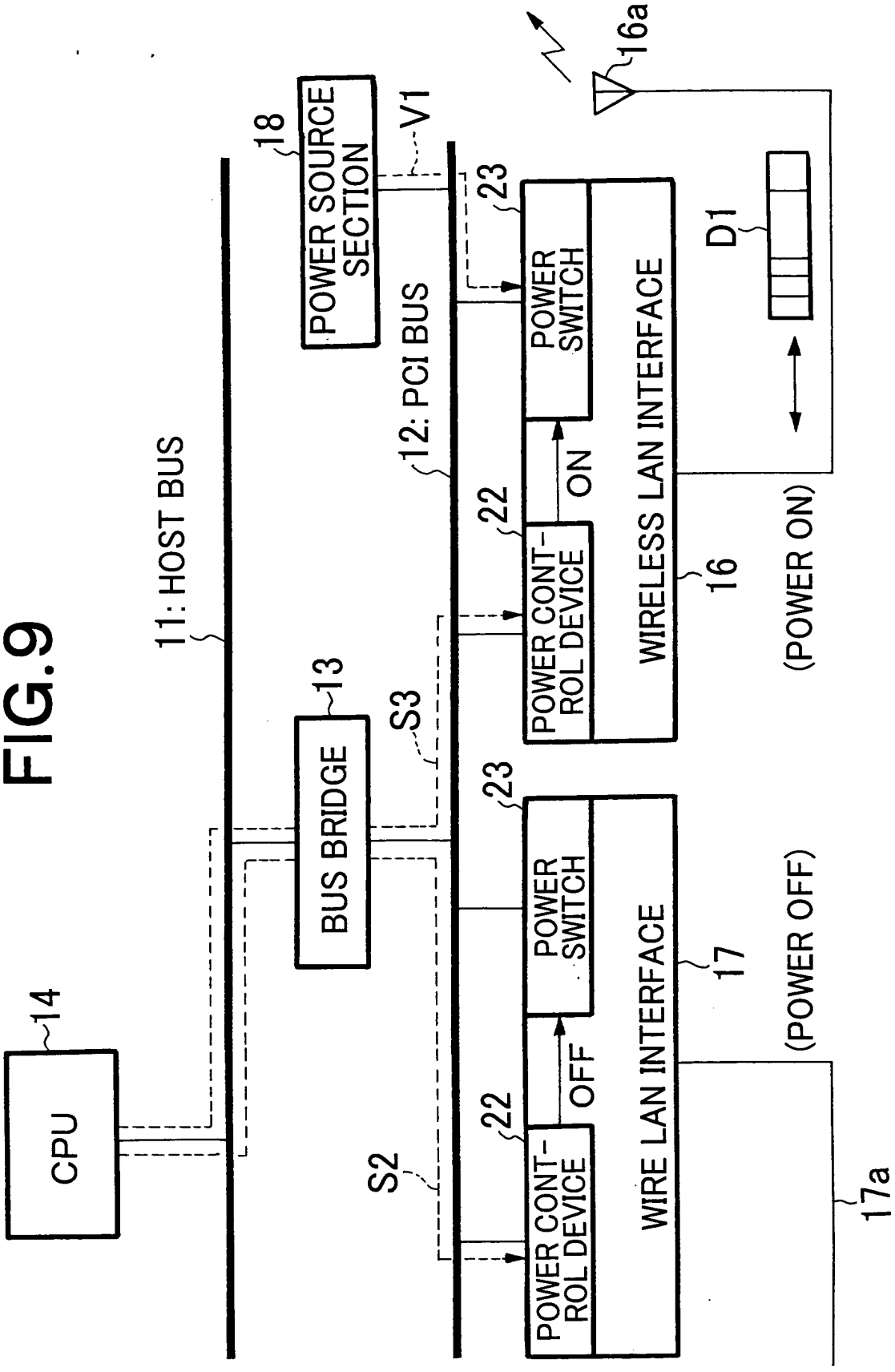

CPU
14
11: HOST BUS
BUS BRIDGE
13
18
POWER SOURCE SECTION
V1
12: PCI BUS
S2
S3
23
POWER SWITCH
22
POWER CONT-ROL DEVICE
OFF
WIRELESS LAN INTERFACE
16
16a
(POWER OFF)
ON
WIRE LAN INTERFACE
17
(POWER ON)
D2
17a

FIG.9


CPU
14
11: HOST BUS
BUS BRIDGE
13
12: PCI BUS
18
POWER SOURCE SECTION
V1
16a
D1
S2
S3
22
POWER CONT-ROL DEVICE
OFF
ON
23
POWER SWITCH
WIRE LAN INTERFACE
WIRELESS LAN INTERFACE
17
16
17a
(POWER OFF)
(POWER ON)